Docket No.: M4065.0536/P536-B

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Hongmei Wang et al.

Application No.: NOT YET ASSIGNED

Confirmation No.:

Filed: January 6, 2004

Art Unit: N/A

For:

FULLY-DEPLETED (FD) (SOI) MOSFET

ACCESS TRANSISTOR AND METHOD

OF FABRICATION

Examiner: Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT (IDS)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement accompanies the new patent application submitted herewith.

Those patents or publications which are marked with an asterisk (*) in the attached form PTO/SB/08 (facsimile) are not supplied because they were previously

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cited by or submitted to the Office in a prior application no. 10/303,696, filed November 26, 2002, which is a divisional of application no. 10/161,615, filed June 5, 2002, and relied upon in this application for an earlier filing date under 35 U.S.C. 120.

Copies of the references on the PTO/SB/08 are not provided.

A concise explanation of relevance of the items listed on form PTO/SB/08 is given.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. In accordance with 37 CFR 1.97(h), the filing of this Information Disclosure statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper

hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0536/P536-B.

Dated: January 6, 2004

Respectfully submitted,

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PTO/SB/08a/b (08-03)
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Substitute for form 1449A/B/PTO				Complete if Known		
"				Application Number	NOT YET ASSIGNED	
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STATEMENT BY APPLICANT (Use as many sheets as necessary)				First Named Inventor	Hongmei Wang	
				Art Unit	N/A	
				Examiner Name	Not Yet Assigned	
Sheet	1	of	1	Attorney Docket Number	M4065.0536/P536-B	

	U.S. PATENT DOCUMENTS							
Examiner Initials*	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
		Number-Kind Code ² (if known)						
	AA	5,340,759	08-1994	Hsieh et al.*				
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T⁵

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NON PATENT LITERATURE DOCUMENTS						
Examiner Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²			
	CA	S.M. Sze, "Physics of Semiconductor Devices," John Wiley & Sons, New York, (1981), pp. 454-455.*				
	СВ	Syd R. Wilson, et al., "Handbook of Multilevel Metallization for Integrated Circuits," Noyes Publ., Westwood, New Jersey, (1993), pp. 259-264.*				
	СС	Gary E. McGuire, "Semiconductor Materials and Process Technology Handbook," Noyes Publ., Norwich, New York, (1988), pp. 46-47.*				
	CD	Julian J. Sanchez, et al., "Drain-Engineered Hot-Electron-Resistant Device Structures: A Review," IEEE Trans. On Electron Devices, Vol. 36, No. 6, (1989), pp. 1125-1132.*				

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English language Translation is attached.

Examiner	 Date	
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